

### Amendments to the Specification

Please replace the paragraph beginning on page 7, line 12, with the following amended paragraph, where added material is shown in underlined type, deleted material is shown in ~~strikeout type~~:

The incoming serial channels 20, which are not shown in Fig. 2, are each connected to a respective data bus 60. The data bus 60 is adapted to the size of the serial channel and, in the present example, carries a 16-bit data stream.

*B1*  
The data bus is connected in parallel to the input, or ~~write, ports~~ write ports of all RAM cells in both arrays 31, 32 (see Fig. 3) of the storage element 30 associated with the incoming channel 20. The output ports of all memory cells in one row, i.e. of 16 memory cells across the whole converter shown in Fig. 1, are connected in parallel to the read-out amplifier 40.